Journal of Integrated Circuits and Systems

Volume 3 • Number 1 • March 2008 • ISSN 1807-1953

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SBC Sociedade Brasileira de Computação

www.sbc.org.br/jics
www.sbmicro.org.br/jics

ISSN 1807-1953
9 771807 195008

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The **Journal of Integrated Circuits and Systems** (**JICS**) is a publication from SBMicro – Brazilian Microelectronics Society and SBC – Brazilian Computer Society whose are non-profit scientific societies aiming to foster the microelectronics and computer science teaching, research and development.

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FOREWORD

This issue of Journal of Integrated Circuits and Systems (JICS) includes 6 papers on design, test and CAD. Some of them have been selected from the presentations given at SBCCI2007 (21st Symposium on Integrated Circuits and Systems Design), which has been held in Rio de Janeiro, Brazil in 2007. Among the contributions presented at SBCCI2007 conference, only a few best rated by the reviewers were selected by the JICS Editorial Board and have been invited to submit an extended version. These papers have been submitted to usual reviewing process with the help of external experts.

We would like to thank the authors for their effort in preparing these high quality papers, as well as the reviewers for their help on paper selection, which guarantees the scientific level of this issue. We sincerely hope that JICS readers will enjoy these contributions, as we have enjoyed listening to them at the SBCCI2007 conference.

This year the JICS journal will restart the spontaneous submission of papers additionally to the invitation of best papers of Symposium on Microelectronics Technology and Devices and Symposium on Integrated Circuits and Systems Design. We hope the community will support our decision submitting high quality papers that will contribute to the excellence of JICS Journal.

João Antonio Martino
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