

DERIVATIVE APPROXIMATION FOR ANALOG DESIGN

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ABSTRACT

This paper presents a technique of derivative approximation in the design of analog circuits, which can be useful in the implementation of a function derivative. We present an example and discuss some aspects involved in the application of this technique.

1. INTRODUCTION

Analog circuit design is an important aspect in many applications, such as artificial neural networks, fuzzy systems, and as a support for digital circuits [1]. In the design of analog circuits we can employ several techniques and mathematical resources to obtain the implementation of a certain function. For example, at the device level, the MOS transistor can be exploited in the various regions of operation. In saturation, it operates as a quadratic function; in subthreshold, as an exponential, and in triode, as a resistor. At the circuit level, functions such as addition, subtraction, multiplication, derivative and integral can be exploited. This work presents a derivative approximation in the design of analog circuits. This technique has been successfully employed in the design of radial basis circuits [2]-[4]. We present an example and discuss some aspects involved in the application of the technique.

2. DERIVATIVE APPROXIMATION

Consider the expression of the derivative $f'(x)$ of a function $f(x)$:

$$f'(x) = \lim_{\Delta \rightarrow 0} \frac{f(x + \Delta) - f(x)}{\Delta} \quad (1)$$

We can rearrange (1) in the following way:

$$f'(x) \cong \frac{1}{\Delta} [f(x + \Delta) - f(x)] + \varepsilon \quad (2)$$

where ε is an error term and $1/\Delta$ is an amplitude gain. Equation (2) can be applied to any differentiable function.

We note that the derivative function can be approximated by a difference operation with a shift in inputs. The approximation will be closer to the derivative function the smaller the shift. Furthermore, if we eliminate the factor $1/\Delta$ and the term ε , we conclude that the function $g(x)$ given by:

$$g(x) = [f(x + \Delta) - f(x)] \quad (3)$$

has the form of the derivative function except by a gain factor $1/\Delta$ and an error term ε .

3. EXAMPLE: A HYPERBOLIC SECANT SQUARED

The differential pair is one of the most used blocks in analog circuits. We will present the application of the derivative approximation technique in the design of a circuit that implements the transconductance function of an emitter-coupled pair (Fig 1). The differential current I_d and the differential voltage V_d are related by:

$$I_d = \alpha_F \cdot I_t \cdot \tanh(V_d / 2V_t) \quad (4)$$

where I_t is the tail current, α_F is the relation I_C/I_E and V_t is the thermal voltage. The transconductance g_m is given by equation (5):

$$g_m = \frac{\partial I_d}{\partial V_d} = \frac{\alpha_F \cdot I_t}{2V_t} \cdot \text{sech}^2 \left(\frac{V_d}{2V_t} \right) \quad (5)$$

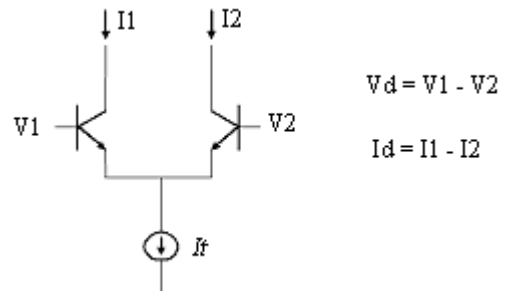


Fig. 1: Emitter-coupled pair.

The output of the differential pair is a hyperbolic tangent function (Fig. 2). The transconductance curve is its first derivative and has the form of a hyperbolic secant squared (Fig. 3).

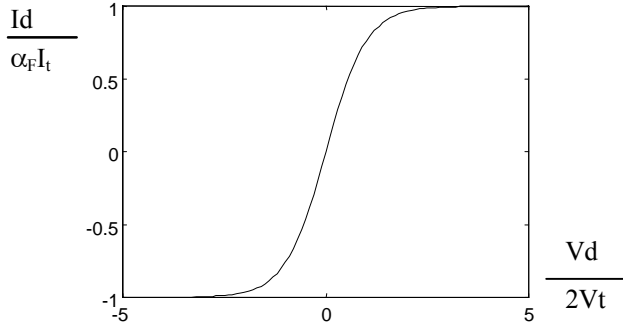


Fig. 2: DC Transfer Characteristic of an Emitter-Coupled Pair.

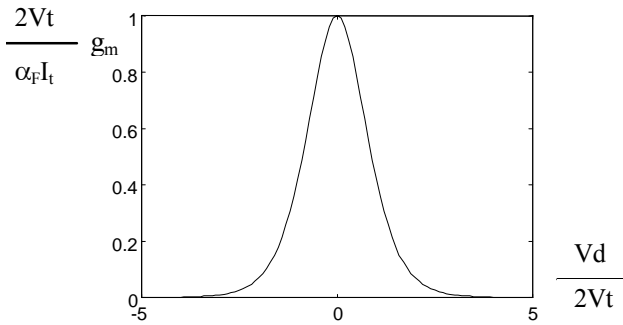


Fig. 3: DC Transconductance Characteristic of an Emitter-Coupled Pair.

The circuit that realizes the derivative function is presented in Fig. 4 (see appendix). The experimental data presented in this paper were obtained with a breadboard prototype. The differential pairs were implemented with OTAs presented in the LM13600 dual-OTA integrated circuit. The output voltage V_{out} is obtained by the difference of OTA1 and OTA2 outputs which is implemented by current-mode summing at the resistor R_o . I_{b1} and I_{b2} allow the amplitude adjustment. The currents are in the milliamp range. The voltage shift v_{delta} in the input of OTA2 is fixed in 100mV. OTA3 is a multiplier and allows width adjustment by I_{b3} and center adjustment by V_c .

4. RESULTS

Fig. 5 shows the experimental results for the hyperbolic secant squared implementation. Fig. 6 shows the circuit response for triangular signal in the input.

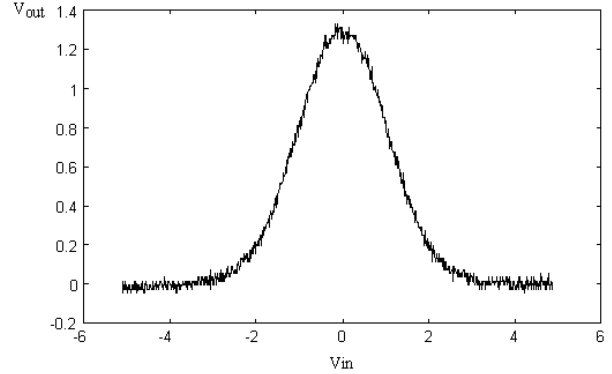


Fig. 5: Experimental data obtained for the circuit.

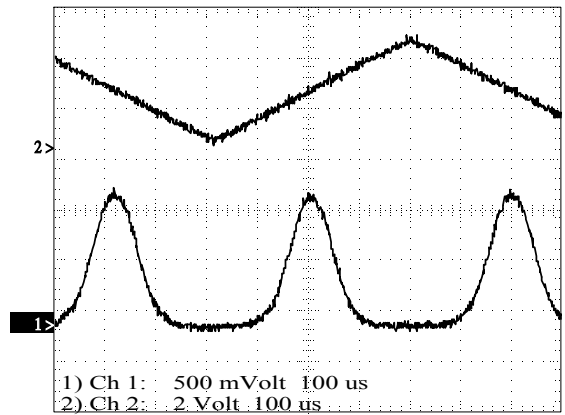


Fig. 6: Circuit response for a triangular input signal.

5. CONCLUSIONS

The proposed technique allows the realization of the derivative of a circuit response. An example of application where this technique could be useful is in the implementation of functions for artificial neural networks with on-chip learning. Finally, the technique can be a part of the repertoire available to the designer in the implementation of analog functions.

6. ACKNOWLEDGMENTS

We would like to thank CAPES and IAE – Instituto de Aeronáutica e Espaço for the financial support.

7. REFERENCES

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8. APPENDIX

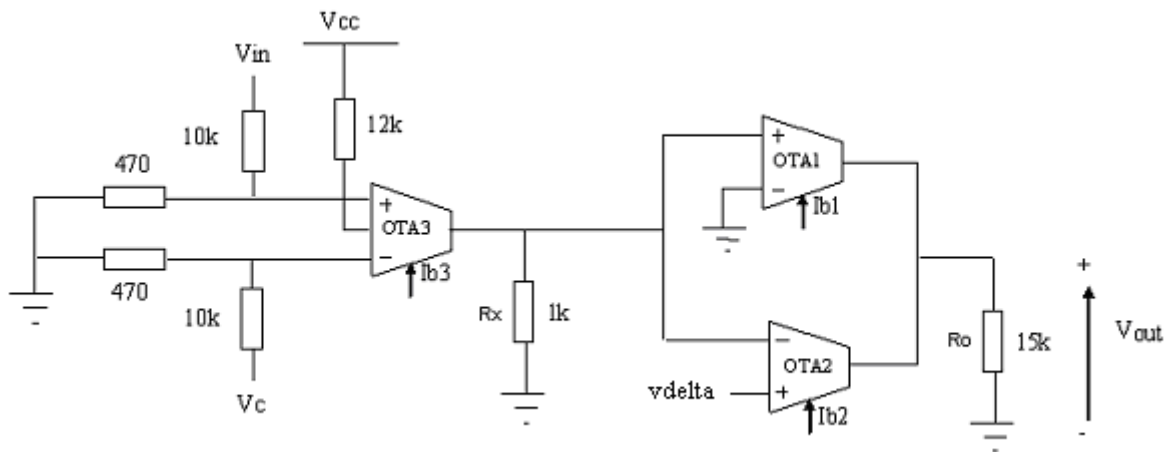


Fig. 4: The Hyperbolic Secant Squared circuit.