

SForum 2006

Monday (August 28th)

#	Time	
*	08:00	REGISTRATION STARTS

Tuesday: Tutorial I

#	Time	Title and Tutorialist
*	14:00	ADVANCED ULTRA LOW-POWER BIOMEDICAL SIGNAL PROCESSING BASED ON ANALOG WAVELET TRANSFORM S. Haddad - Delft University of Technology, The Netherlands

Tuesday: Session I (oral presentations)

#	Time	Title and Authors
1	15:50	KARMA: A DIDACTIC TOOL FOR KARNAUGH MAPS CARLOS E. KLOCK, FELIPE R. SCHNEIDER, MATEUS V. N. GOMES, RENATO P. RIBAS and ANDRE I. REIS
2	16:10	EFFICIENT SIMULATION OF SINGLE-ELECTRON DYNAMICS IN COUPLED QUANTUM DOTS Andre Calmon, Marcus Batistuta and José Camargo
3	16:30	NUMERICAL ANALYSIS OF AN InSb INFRARED RADIATION PHOTODETECTOR Felipe Lorenzo Della Lucia and Jacobus Swart
4	16:50	LOW POWER DEDICATED DATAPATHS FOR LMS ADAPTIVE FILTER DOUGLAS FOSTER, NATHALIA ARDIZZONE, MARCELO FONSECA, SERGIO MELO and EDUARDO COSTA
5	17:10	BLUEGUM: USE OF BLUETOOTH INTERFACES IN THE ACCESS OF PORTABLE EQUIPMENT Karen Oliveira, Emilena Specht and Luigi Carro
6	17:30	INITIAL HIGH LEVEL CACTI ESTIMATES AND PHYSICAL DESIGN IMPLEMENTATION OF FAST SRAM CACHES Eduardo Conrad Junior, Eric Ericson Fabris and Sergio Bampi

POSTER SESSION: SForum

Tue. Aug. 29th, 17:50-19:30, Wed. Aug. 30th, 10:10-10:40 and 15:20-15:50 (during coffee breaks)

#	Title and Authors Poster presenters are strongly encouraged to stand by their posters at all times indicated
<i>PSF1</i>	AUTOMATIC ADL-BASED GENERATION OF DISASSEMBLING TOOLS Alexandre K. I. Mendonça, Felipe G. Carvalho, Max R. de O. Schultz, Luiz C. V. Santos and Olinto J. V. Furtado
<i>PSF2</i>	TRANSISTOR PLACEMENT FOR NONDUAL CMOS CELL SYNTHESIS Mário C. B. Osório, Renato P. Ribas and André I. Reis
<i>PSF3</i>	CELL LIBRARY VERIFICATION METHODOLOGY Simone Bavaresco, Caio G. P. Alegretti, Renato P. Ribas and André I. Reis
<i>PSF4</i>	A VLSI PROTOTYPE OF A 32-BIT RISC PROCESSOR Flávio Calmon
<i>PSF5</i>	ENTROPY CODER ARCHITECTURES FOR H.264/AVC BASELINE PROFILE Thaísa Silva, João Vortmann, José Güntzel, Sergio Bampi and Luciano Agostini
<i>PSF6</i>	AN IMPROVED PARAMETER EXTRACTION METHOD USING A RF SOFTWARE SIMULATION Fellipe Grillo Peternella, Leandro Tiago Manera and José Alexandre Diniz
<i>PSF7</i>	PALM- BASED INTERFACE FOR PORTABLE POTENTIOSTAT Luiz Menezes, Juliana Cardoso and Marcelo Bariatto
<i>PSF8</i>	IMAGE MOSAIC CONSTRUCTION ON DSP Yumi Monma, Thiago Figueiró, André Soares, Letícia Guimarães and Altamiro Susin
<i>PSF9</i>	DESIGN OF AN ALL-DIGITAL PHASE LOCKED LOOP: ANALYSIS AND CHARACTERIZATION IN FPGAS Luciano de Paula, Altamiro Susin and Sergio Bampi
<i>PSF10</i>	Automatic Analysis of SET Propagation in CMOS Combinational Circuits Carolina Neves, José Luís Güntzel and Fernanda Kastensmidt
<i>PSF11</i>	LEAKAGE CURRENT IN SUBMICRON PARTIALLY DEPLETED SOI NMOSFETS OPERATING AT HIGH TEMPERATURES Luciano Mendes Almeida and Marcello Bellodi
<i>PSF12</i>	DEVELOPMENT OF A LASER ULTRASOUND SYSTEM FOR ELASTIC CHARACTERIZATION OF MICROSTRUCTURES Thiago Rueda, Cassius Ramos, Ednei do Amaral, Henrique Menegaz and Demartonne Ramos França

<i>PSF13</i>	OPTOELECTRONIC PROPERTIES OF CERAMIC SEMICONDUCTOR FILMS Frederico Mendes, Felipe Cândido, Déborah Alvarenga and Wagner Rodrigues
<i>PSF14</i>	The Successive Refinement Methodology in the Embedded Systems Design Sandro Renato Dias and Diógenes Cecílio Silva Júnior
<i>PSF15</i>	CATHETER BASED BIOSENSOR:ASSEMBLING AND CHARACTERIZATION Fernando Luis de Almeida and Marcelo Bariatto Andrade Fontes
<i>PSF16</i>	HEART BEAT DETECTION USING WAVELET TRANSFORM Marcelo S. Stoco, Rodrigo V. Andreão and Marcelo V. Segatto
<i>PSF17</i>	Integrated Digital Systems Design Methodology Ramon Risério and Diógenes Cécilio Silva Jr
<i>PSF18</i>	TEST STRUCTURES TO EVALUATE A CELL LIBRARY Carlos Afonso Ferreira Silva, Giovanni Sartori, André Inácio Reis and Renato Perez Ribas
<i>PSF19</i>	A VLSI IMPLEMENTATION OF A LINEAR CELLULAR AUTOMATA TEST-BED Flávio Calmon and Felipe Miranda
<i>PSF20</i>	WIRELESS ECG MONITORING WITH PARTIAL TIME TRANSCEIVER USE Emilio Bumachar, Rodrigo Andreão and Marcelo Segatto
<i>PSF21</i>	COARSE-GRAINED RECONFIGURABLE ARCHITECTURES Alisson Garcia and Ricardo Ferreira
<i>PSF22</i>	CHARGE DISTRIBUTION IN TRIPLE-GATE DEVICES AT THRESHOLD VOLTAGE Rodrigo Bechelli and Renato Giacomini
<i>PSF23</i>	BDDeiro: BDD Visualization Tool Mateus V. N. Gomes, Carlos E. Klock, Tiago M. G. Cardoso, Renato P. Ribas and André I. Reis
<i>PSF24</i>	Fabrication and Electrical Characterization of MOS Capacitors - Chemical Cleaning Evaluation Rodrigo Marques and Victor Sonnenberg
<i>PSF25</i>	PDesigner Framework Millena Gomes, Williams Azevedo, André Souza and Renato Oliveira