

WCAS2016

6th WORKSHOP ON CIRCUITS AND
SYSTEMS DESIGN

CHIP ON THE MOUNTAINS

Co-located Symposia:

29th SBCCI – Circuits and Systems Design
31st SBMicro – Devices and Processes

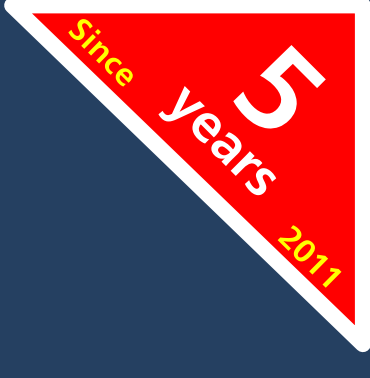
1st INSCIT – Electronic Instrumentation
16th SForum – Undergraduate-student Forum



Belo Horizonte, Minas Gerais, BRAZIL

August 29 to September 3, 2016

www.wcas.org.br



WCAS is devoted to the presentation and discussion of design experiences with a high degree of relevance in industrial, educational, and research contexts, as well as innovative design methodologies and application of specific design technologies in an industrial context.

The workshop is organized within two tracks: industrial and academic. Contributions should illustrate state-of-the-art designs, design methodologies or tools, which will provide viable solutions in tomorrow's silicon and embedded systems. The contribution should present electronic circuit and system design results from FPGA and/or ASIC prototypes, or from simulation. Designs that achieve a specific record in terms of performance, power management or any other concrete advantage compared to the state-of-the-art for a given application domain should also be submitted to this workshop. Methodologies and techniques validated through demonstration are welcome. The author(s) of the best papers will be invited to submit an expanded version to be considered for publication in the at JICS - Journal of Integrated Circuits and Systems.

General Chair:

Davies William de Lima Monteiro
UFMG, Brazil

Program Chairs:

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Panels Chair:

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Unitec Semiconductors, Brazil

Finance Chairs:

Ado Jório, UFMG, Brazil
Luciana P. Salles, UFMG, Brazil

Publication Chair:

Tales C. Pimenta, MinasTec, Brazil

For Program Committee Members,
please see the conference webpage

Paper Submission:

The authors are invited to submit a 2-4 page manuscript, in double column following the IEEE conference format and be prepared for **blind review**. The text must focus on the presentation of results and contain essential information with sufficient detail to be understood technically. The inclusion of measured results for key parameters, schematics, and a tabulated comparison with the state-of-the-art are strongly recommended. For papers on integrated circuits submitted to the industrial track, results from silicon implementation, including a die photograph, are mandatory. For academic submissions, results from simulations will also be accepted, provided that the simulations take into account non-idealities that will affect the practical realization of the circuit. Other contributions on design methodology and also proof of concept using discrete electronics are welcome. The accepted papers will be presented in poster or oral sessions.

The areas of interest include, but are not limited to:

- 1 - Analog & RF & Mixed Signal
- 2 - EDA, Verification and Test
- 3 - Digital, Reconfigurable & Applications
- 4 - SoC, NoC, Embedded
- 5 - Industrial Application of ASICs (Computing, Communication, Consumer, Transportation, Medical/Healthcare, Energy Generation, Recovery, and Management Systems, and others)

Location:

Belo Horizonte, translated as beautiful horizon, is the capital of the State of Minas Gerais, located 843 m above sea level, full of greenery and with a pleasant atmosphere. The urban infrastructure of this 118-year-old city combines modernity to tradition and houses around 2.5 million inhabitants who benefit from a range of entertainment options. Beagá, as it is also graciously called, is situated in a strategic location within Brazil as close as a one-hour flight from Rio de Janeiro, São Paulo or Brasília, being considered an important hub to the national air network. The city offers more bars per capita than any other major Brazilian city, serving an exclusive range of options for drinks and food, especially the famous regional cachaça, the main ingredient for the world-famous caipirinha. The surrounding area is rich with unspoiled natural areas, including caves, waterfalls and tropical biospheres. There is also direct access to historical colonial cities, as Ouro Preto, and to the exquisite open-air art-gallery complex amid landscaped gardens, Inhotim. The State of Minas Gerais is active in various industrial sectors, agricultural crops and cattle handling, having also staged a history in the microelectronics national scenario. The state is currently re flourishing both industrially and academically in the semiconductor, nanotechnology, photovoltaic and microelectronics fields, providing exclusive access to yet untapped markets. We will be pleased to host you and your colleagues in Belo Horizonte in 2016 to explore possible innovation horizons.

Important Dates

Paper Submission Deadline:

June 5th, 2016

Notification of Acceptance:

July 7th, 2016

Camera-Ready Deadline:

July 29th, 2016



www.chip-on-the-mountains.com, chip@dcc.ufmg.br

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