# VCAS 2022

12<sup>nd</sup> Workshop on Circuits and Systems Design

August 22 to 26, 2022 Virtual from Porto Alegre, Brazil





#### **General Chairs**

Bruno Zatt, UFPel, Brazil José Rodrigo Azambuja, UFRGS, Brazil

#### **Program Chairs**

Arthur Liraneto Torres Costa, CESAR, Brazil Sandro Binsfeld Ferreira, UNISINOS, Brazil

#### **Tutorials Chair**

Michelly de Souza, FEI, Brazil Antonio Carlos Beck Filho, UFRGS, Brazil

#### **Publicity Chairs**

Jacobus Swart, UNICAMP, Brazil Linnyer Ruiz, UEM, Brazil Cláudio Diniz, UFRGS, Brazil

### **Publication Chair**

André Mariano, UFPR, Brazil

# **Panels Chair**

Marcelo Lubaszewski, UFRGS, Brazil

#### **Finance Chairs**

Daniel Palomino, UFPel, Brazil Paulo Butzen, UFRGS, Brazil Guilherme Corrêa, UFPel, Brazil

# **Streaming Chairs**

Fábio Ramos, UNIPAMPA, Brazil Leomar Rosa Jr., UFPel, Brazil Raphael Brum, UFRGS, Brazil

### Liaison Americas / Asia / Europe

Fernando Guarin, Global Foundries, USA Hiroshi Iwai, TIT, Japan Cor Claeys, KU Leuven, Belgium

## Co-located Events:

36th SBMicro, Symposium on Microelectronics Technology

35th SBCCI, Symposium On Integrated Circuits And Systems Design

22nd SForum, Microelectronics Students Forum

6th INSCIT, International Symposium on Instrumentation Systems, Circuits and Transducers

Workshop on Circuits and System Design – WCAS 2022 is devoted to the presentation and discussion of design experiences with a high degree of relevance in industrial, educational, and research contexts, as well as innovative design methodologies and application of specific design technologies in an industrial context. The workshop is organized within two tracks: industrial and academic, stimulating the interaction between players from different environments, from silicon to embedded systems, from universities and small to large companies. Contributions should illustrate state-of-the-art designs, design methodologies, or tools, which will provide viable solutions in tomorrow's silicon and embedded systems. The contributor is encouraged to present electronic circuits, systems, and product engineering design results from FPGA, MCU-based, RF-IoT and/or ASIC prototypes. Designs that achieve a specific record in terms of performance, power management, or any other concrete advantage compared to the state-of-the-art or that achieve a specific innovative level and social impact in the technology market for a given application domain should also be submitted to this workshop. Validated methodologies and techniques through demonstration are welcome. The contribution in terms of local/regional/global social impact is welcome.

The areas of interest include but they are not limited to:

- Analog & RF & Mixed Signal
- EDA, Verification and Test
- Digital, Reconfigurable & Applications
- SoC, NoC, Embedded Systems
- Industrial Application of ASICs (Computing, Communication, Consumer, Transportation, Automation, Medical/Healthcare, Energy Generation, Recovery, and Management Systems, and others)

May 8<sup>th</sup>, 2022

June 24th, 2022

# Sponsored by:

ABISEM, Brazilian Semiconductor Industry Association **EnSilica** 

Smart

Chipus

Organized by:

UNISINOS, Universidade do Vale dos Sinos CESAR, Centro de Estudos e Sistemas Avançados do Recife







