**IMEC Free Mini@sic Fabrication on TSMC 0.18 um Technology**

Title of the Circuit:

Adviser Professor(s)

Students involved (names and aimed degrees)

Institution (University/Faculty)

Type of Circuit Design: ( ) Digital; ( ) mixed signal; ( ) analog; ( ) RF

Date of the circuit tape-out of the run:

Date of receiving the chips at your institution:

Date of the report and/or of later update:

Short description of the circuit: (function, proposed innovation, number of transistors, passive components, screen shot of the chip layout, etc.)

Main results and representative performance data or curves, picture of the IC, does circuit function as expected, etc.

Main challenges and difficulties encountered during design, submission, import process and measurements of the circuit.

Resulting publications (submitted, accepted or published) and degrees earned by students.

Notes:

1. Publications should include acknowledgement to imec for the free fabrication on TSMC 180 nm technology.
2. The length of the report may be 2 to 3 pages.
3. The report is due at 6 months after receiving the chips (can be updated afterwards, with new publications and students completing degrees).
4. Any suggestion to improve the program can be sent separately to Jacobus Swart.