**Guidelines for Design selection for free mini@sic fabrication by IMEC on 180nm TSMC**

**Criteria from the call:**

**Motivation:**

* Brazil has several incentive programs for the stimulation of ASIC design and has set up several design houses
* Design houses need experienced IC designers
* Through the IC-Brazil Program several hundred designers have been trained and employed by the design houses
* Before entering the IC-Brazil Program and/or job market it would be beneficial if students at Undergraduate, Master and PhD level could have the chance to design small ICs and have them fabricated and tested

**Who can apply?**

* Professors at Brazilian universities
* For non-funded Master or PhD student designs
* More applications can be submitted, but maximum 1 design per university can be approved for free fabrication

**Suggested information to consider:**

Author of submission:

* Is he a professor at a Brazilian University? Y/N
* Is he an experienced designer: grade from 0 to 10

Involvement of number of students

* Undergraduates
* Master
* PhD

About the IC:

* Relevance: grade from 0 to 10
* Originality or novelty: grade from 0 to 10
* Feasibility: grade from 0 to 10
* Potential for publication: grade from 0 to 10