# **J** ournal of I ntegrated **C** ircuits and S ystems

## Volume 11 $\bullet$ Number 2 $\bullet$

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This second issue is composed by some of the papers that have been selected from those presented at SBMicro2015 (30<sup>th</sup> Symposium on Microelectronics Technology and Devices) and SBCCI2015 SBCCI2014 (28<sup>th</sup> Symposium on Integrated Circuits and Systems Design), which have been held in Salvador, Brazil, in 2015. Among the contributions presented at both Symposia, only a few best rated were selected by the JICS Editorial Board and have been invited to submit an extended version to the Journal. These extended papers have passed through the usual reviewing process before acceptance.

In addition to these selected best papers, spontaneous submissions passed through the usual reviewing process and have been accepted as regular papers.

We would like to thank the authors for their effort in preparing these high quality papers, as well as the reviewers for their valuable contribution on paper evaluation and selection, which guarantees the scientific level of this issue. We sincerely hope that JICS readers will enjoy these contributions.

Marcelo Antonio Pavanello JICS Editor-in-chief Fernanda Kastensmidt JICS Co-Editor