Capa JICS-v4n1-AF 19.08.09 19:16 Page 1

Journal of Integrated Circuits and Systems

Volume 4 • Number 1 • March 2009 • ISSN 1807-1953

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www.sbmicro.org.br/jics

ISSN 1807-1953
9 771807 195008

Sumario e Rostos-v4n1 19.08.09 19:22 Page 1

Journal of Integrated Circuits and Systems

Volume 4 • Number 1 • March 2009 • ISSN 1807-1953

Sumario e Rostos-v4n1 19.08.09 19:22 Page 2

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Sumario e Rostos-v4n1 19.08.09 19:22 Page 3

CONTENTS

Foreword 5

A 40 MHz 70 dB Gain Variable Gain Amplifier Design Using the g_m/I_D Design Method
Fernando Paixão Cortes, Sergio Bampi 7

Study of the Metal Filling Impact on Standard Cells and their Associated Interconnects Using Ring Oscillators: Definition of the Metal Fill Corner Concept
Laurent Remy, Philippe Coll, Fabrice Picot, Philippe Mico, Jean-Michel Portal 13

Secure Triple Track Logic Robustness Against Differential Power and Electromagnetic Analyses
V. Lomné, A. Dehabaoui, T Ordas, P. Maurine, L. Torres, M. Robert, R. Soares, N. Calazans, F. Moraes 20

A Novel AES Cryptographic Core Highly Resistant to Differential Power Analysis Attacks
Felipe Fhellar, Marcelo Soares Lubaszewski 29

Efficient and Flexible Dynamic Reconfiguration for Multi Context Architectures
Julien Lallet, Sébastien Pillement, Olivier Sentieys 36

Sumario e Rostos-v4n1 19.08.09 19:22 Page 4

FOREWORD

This issue of the Journal of Integrated Circuits and Systems (JICS) includes papers on analog design, design for yield, design for security and dynamically reconfigurable architectures. These papers have been selected from the presentations given at SBCCI2008 (21st Symposium on Integrated Circuits and Systems Design), which has been held in Gramado, Brazil in 2008. Among the contributions presented at SBCCI2008, only a few best rated by the reviewers were selected by the JICS Editorial Board and have been invited to submit an extended version. These papers have been submitted to the usual reviewing process with the help of external experts.

We would like to thank the authors for their effort in preparing these high quality papers, as well as the reviewers for their help on paper selection, which guarantees the scientific level of this issue. We sincerely hope that JICS readers will enjoy these contributions.

João Antonio Martino
JICS Editor-in-chief

Marcelo Lubaszewski
JICS Co-Editor

3 Contra Capa-v4n1 19.08.09 19:19 Page 1

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The manuscript must be typed using double spacing and wide A4 paper (3 cm) margins (Top, Bottom, Left, Right) with 12 pt Times New Roman font size up to 18 pages (maximum). Present tables and figure legends on separate pages at the end of the manuscript. All the manuscript pages must be numbered consecutively.

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A self-contained abstract outlining in a single paragraph the aims, scope and conclusions of the paper must be supplied.

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Immediately after the abstract, please provide a maximum of six keywords (avoid, for example, 'and', 'of'). Be sparing with abbreviations: only abbreviations firmly established in the field may be eligible.

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As there is considerable variation in nomenclature and unit systems from country to country, authors are required to include a list of symbols, which they have used in their manuscripts, and of the units in which the corresponding qualities are measured. Another possibility is to define the symbols along the manuscript. SI units should be used where possible.

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All publications cited in the text should be presented in a list of references following the text of the manuscript. Along the text, indicate references by number(s) in square brackets in line with the text.

The papers are submitted online following the link www.sbmicro.org.br/jics.

After acceptance, the author will be notified about the final paper format to be submitted for printing.

Anuncio SBCCI 2 pg- AF 19.08.09 19:27 Page 1

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Anuncio SBCCI 2 pg- AF 19.08.09 19:27 Page 2

SBCCI 2010

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SBCCI is an international forum dedicated to integrated circuits and systems design, test and CAD, held anually in Brazil. The **SBCCI2010** will be located in Sao Paulo, Brazil. The goal of the symposium is to bring together researchers in the areas of computer-aided design, design and test of integrated circuits and systems. The scope of the symposium includes technical sessions, tutorials and panels, as well as exhibition and working group meetings. The best papers presented at the symposium will be invited to resubmit an extended version that will be considered for publication at the IEEE Design&Test of Computers and JICS - Journal of Integrated Circuits and Systems. The **SBCCI2010** will occur in the same venue as SBMicro2010- 25th Symposium on Microelectronics Technology and Devices.

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Topics of interest include, but are not limited to:

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Location:

Sao Paulo The Symposium will be held in the city of Sao Paulo, fondly nicknamed as "Sampa" by the Brazilians. Sao Paulo is a very diverse city with a large number of descendants of immigrants from Italy, Japan, Portugal, Spain, Germany and many other countries. Sao Paulo offers many attractions ranging from historic monuments, museums, music festivals, shopping, and a very dynamic nightlife with a wide range of nightclubs, bars and restaurants, arguably second to none in the world. Within easy reach from Sao Paulo the visitor will find a coastline filled with beautiful beaches as well as ecological parks. Rio de Janeiro can be reached in less than an hour through regular shuttle flights. Iguaçu Falls is also about an hour flight from Sao Paulo.

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Proceedings Published by:
ACM Press

Supported by:
CNPq, CAPES, FINEP, FAPESP

Deadline for Paper Submission:
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Notification of Acceptance:
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